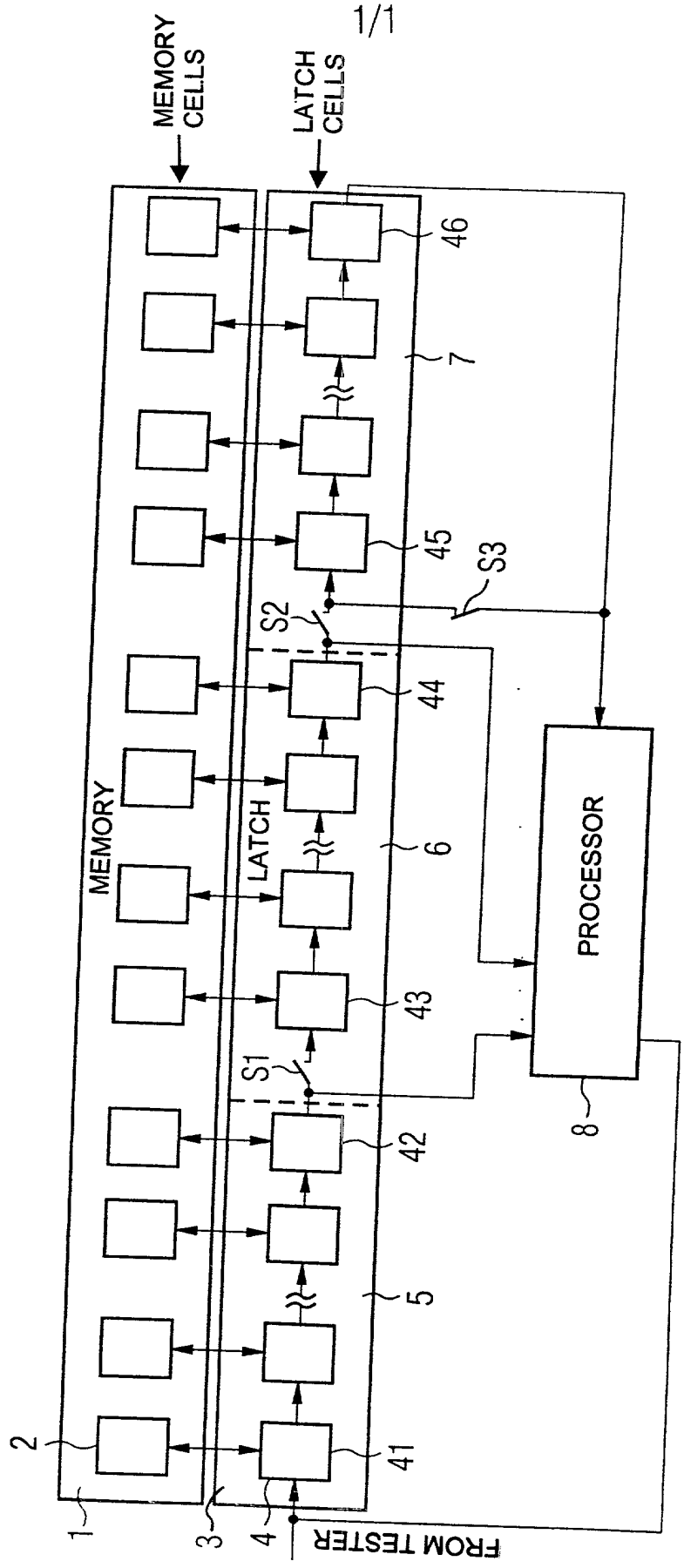
MEMORY CELLS
LATCH CELLS
MEMORY
LATCH
PROCESSOR
FROM TESTER
S1
S2
S3
1
2
3
4
5
6
7
8
41
42
43
44
45
46